

APPLICATION FOR

UNITED STATES LETTERS PATENT

*10/18/01*  
This application ~~claims benefit as~~<sup>is</sup> a divisional of co-pending U.S. Patent Application Serial No. 09/374,971 filed August 16, 1999, entitled "Process for  
5 Producing Printed Circuits and Printed Circuits Thus Obtained" which ~~claimed~~<sup>is continuation of</sup>  
~~benefit as a national stage~~<sup>is</sup> application of PCT Application No. WO 98/36625 filed  
*10/18/01* February 16, 1998, entitled "Process for Producing Printed Circuits and Printed  
Circuits thus Obtained" which claims priority from Italian Application No.  
FI97A000023, filed February 17, 1997. These references are hereby incorporated by  
reference.

I, Antonio Canova, a citizen of Italy, residing at Via Po, 79/A, I-5205  
Mantevarchi; have invented a new and useful "Process for Producing Printed Circuits  
and Printed Circuits thus Obtained."

BACKGROUND OF THE INVENTION

The present invention relates to a process for producing printed circuits  
comprising a laminar support or board, on which is produced a plurality of  
electrically conductive tracks for connecting a plurality of electronic components.  
More particularly this invention relates to a novel technique for applying auxiliary  
20 conductive elements to the laminar support board, having the objective of  
increasing the conductivity of the tracks at specified points in the circuit, where the